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In the specification on page 5, insert after line 17:

49/11/06  
"FIG. 5 is a representation of a further addressing system consistent with certain embodiments of the invention."

In the specification on page 12, insert after line 12:

"An exemplary embodiment of the address calculator 100 of present invention is shown in FIG. 5. Referring to FIG. 5, the address calculator 100 comprises a set of storage elements 102. In one embodiment the storage elements 102 include a TYPE register 104, a STRIDE register 106, a SKIP register 108 and SPAN registers 110 and 110'. The registers are accessed by an arithmetic unit 112. The arithmetic unit may, for example, comprise counters 502, 502' and 504 and a state machine and adder 506. The address pointer 116 (EA) is initialized to the start address denoted by EA\_START. The first counter 502 indicates the number of data elements remaining in the first partition of a first memory level and is initialized to the value LEFT\_START. The first counter 502' indicates the number of data elements remaining in the first partition of a second memory level and is initialized to the value LEFT2\_START. The third counter 504 indicates the total number of data elements to be accessed in the memory and is initialized to the value TOTAL. Once initialized, the arithmetic unit 112 is operable to calculate the address of a current data element in memory from the address of the previous element. The current address (EA) is stored in address pointer 116 and may be output at 118 to access the memory. "